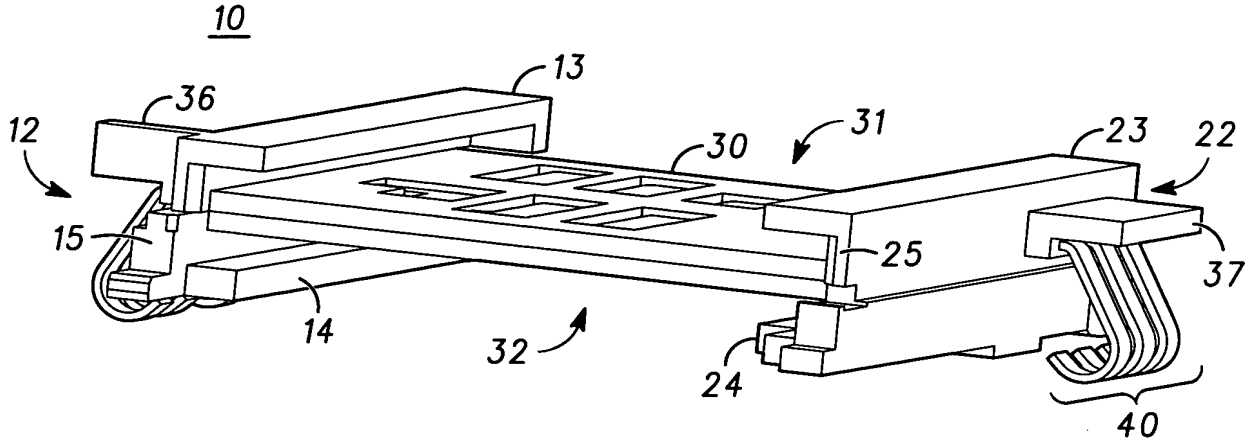
10
13
36
12
30
31
23
22
15
25
37
14
32
24
40

FIG. 1

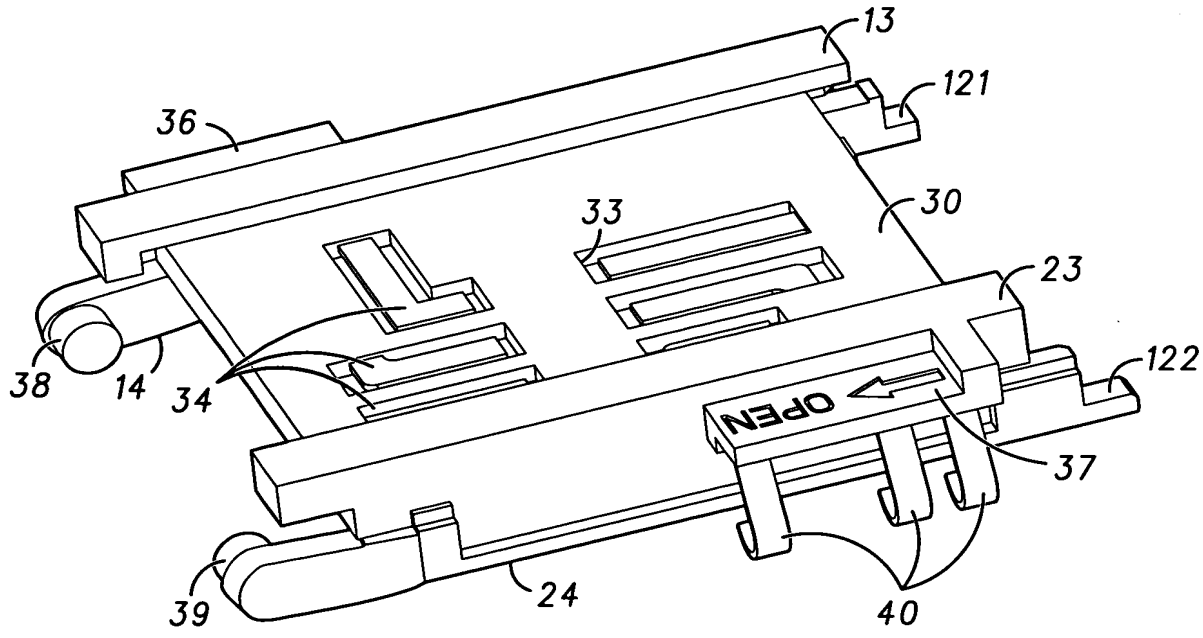
13
121
36
33
30
23
38
14
34
OPEN
122
37
39
24
40

FIG. 2

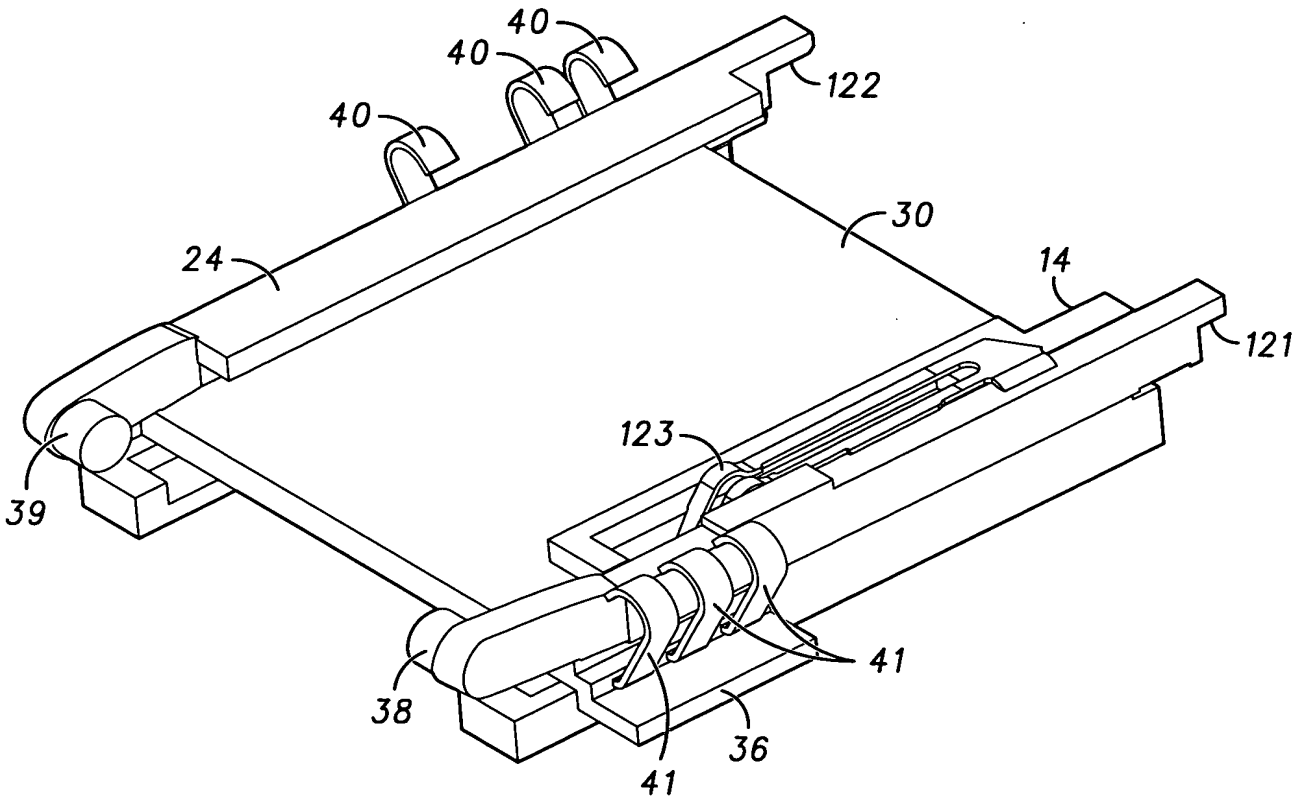

FIG. 3

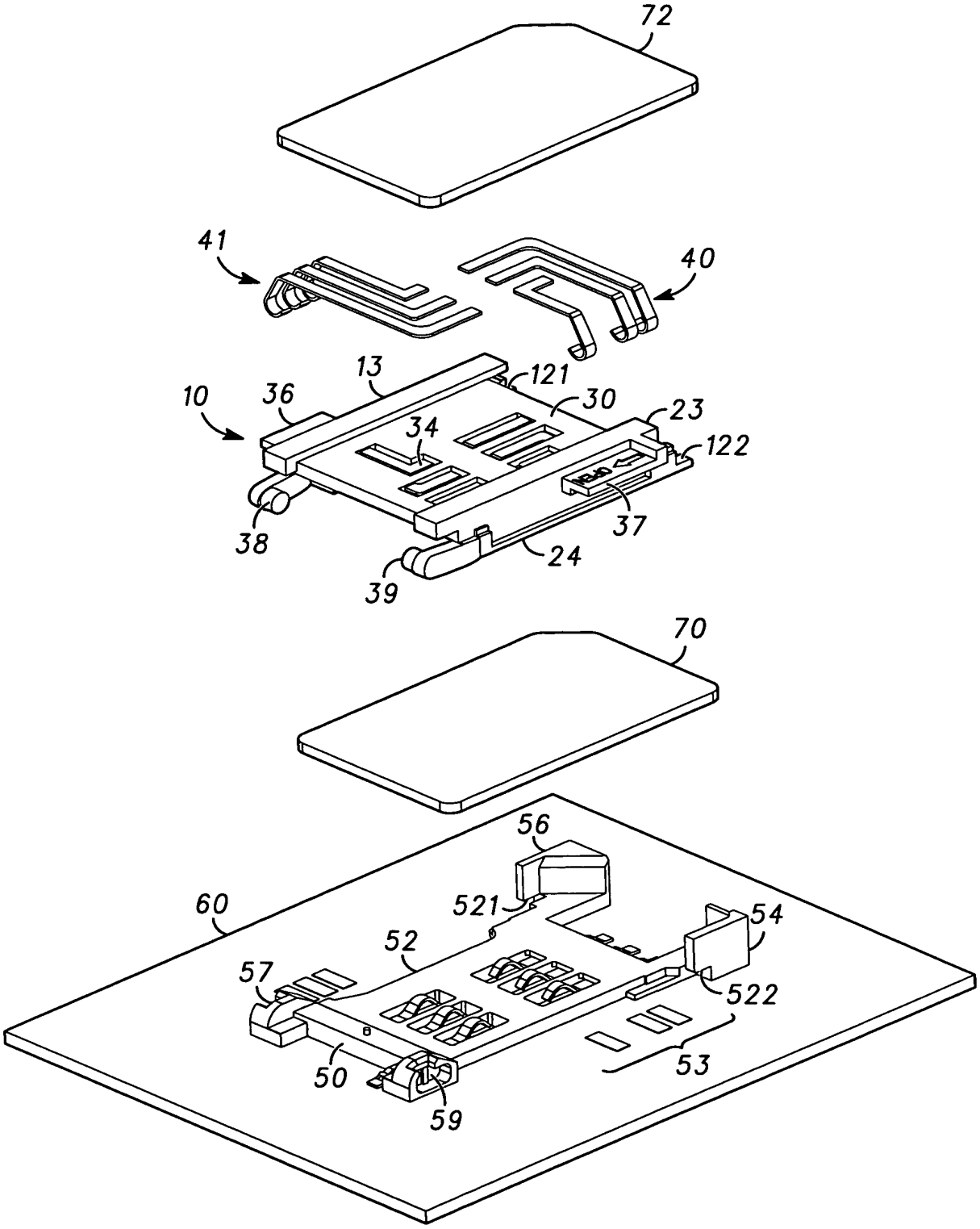
72
41
40
13
121
10
36
30
23
34
122
38
37
24
39
70
56
60
521
54
52
57
522
53
50
59

FIG. 4

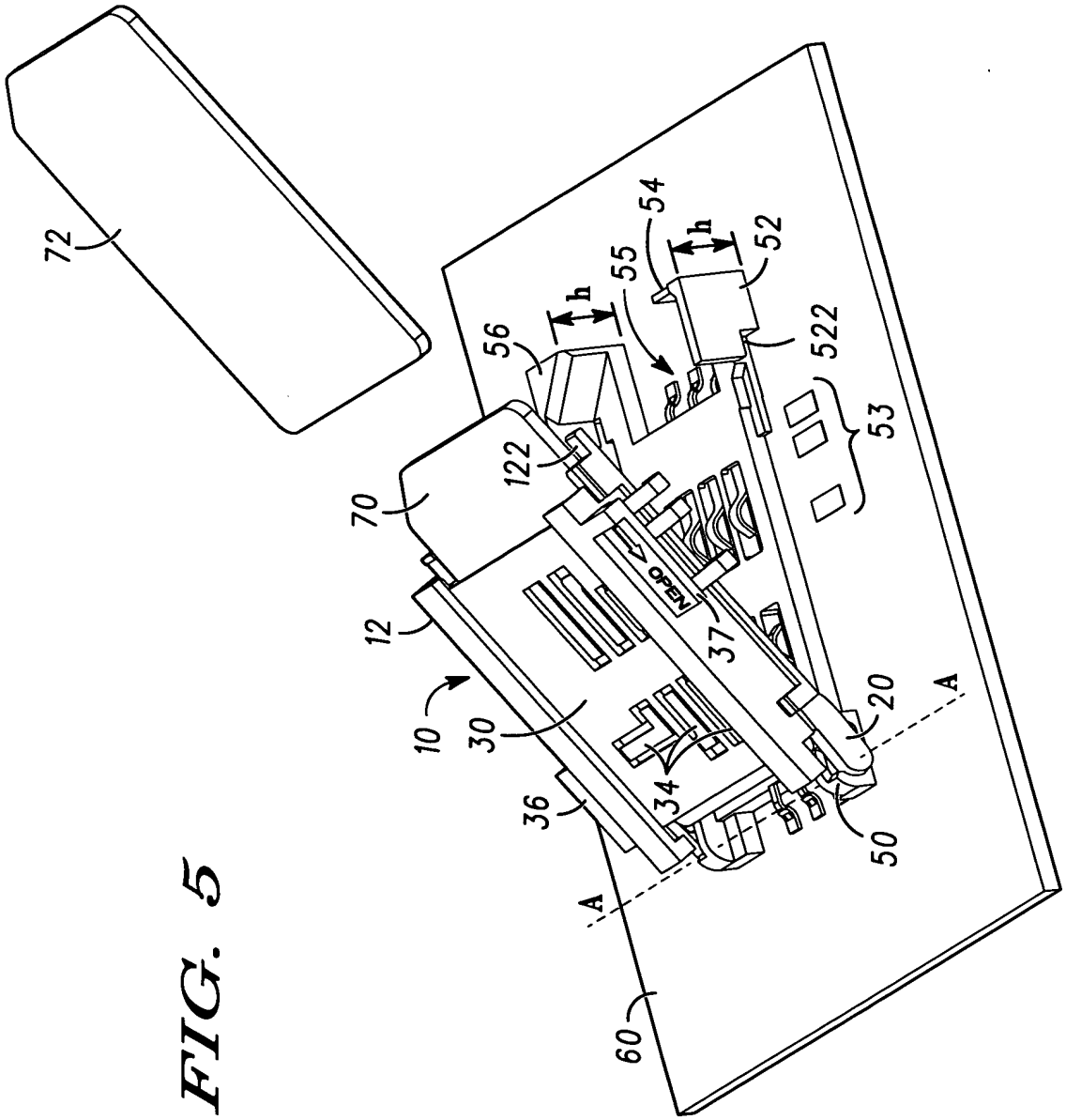

FIG. 5

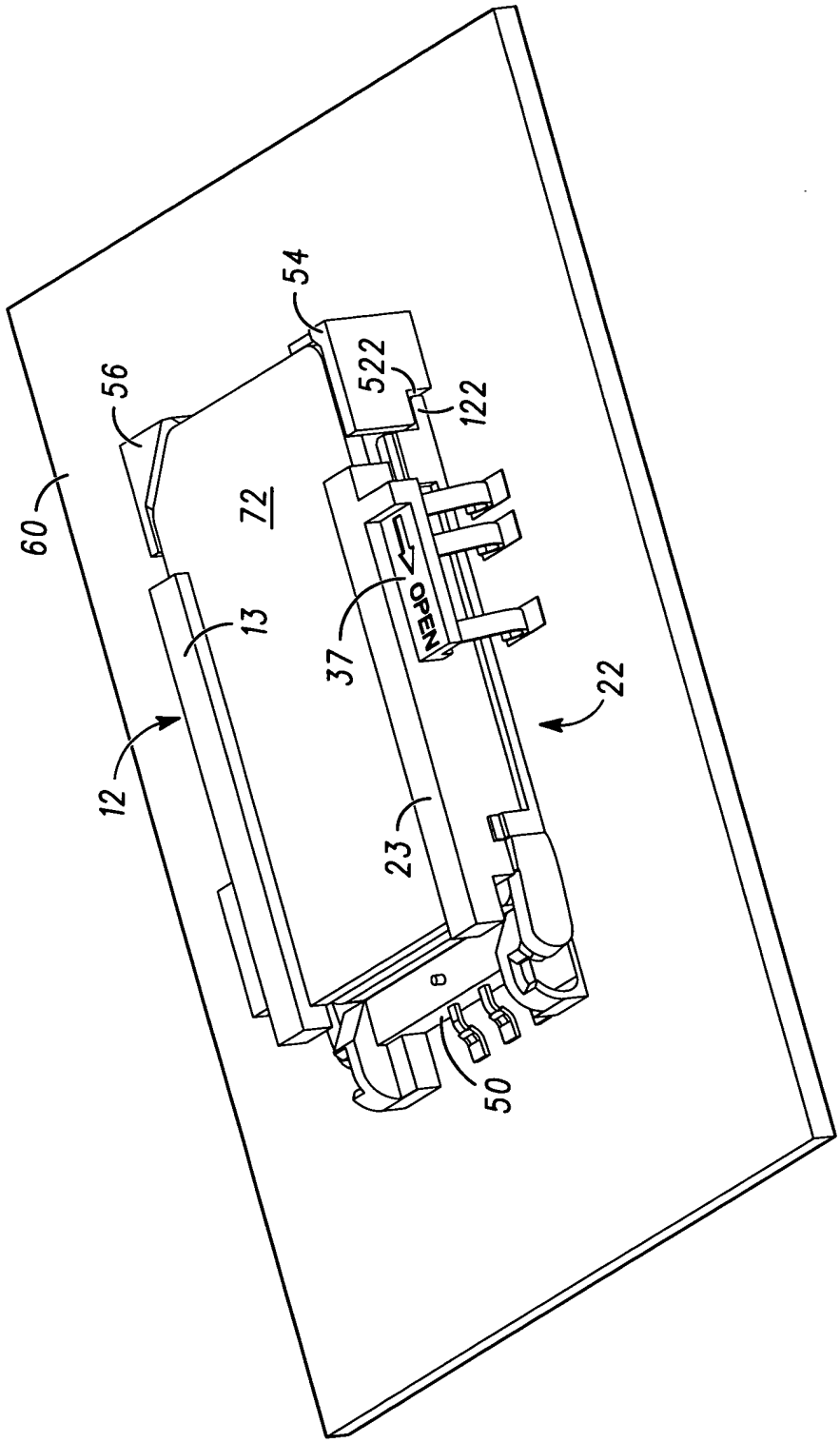

FIG. 6

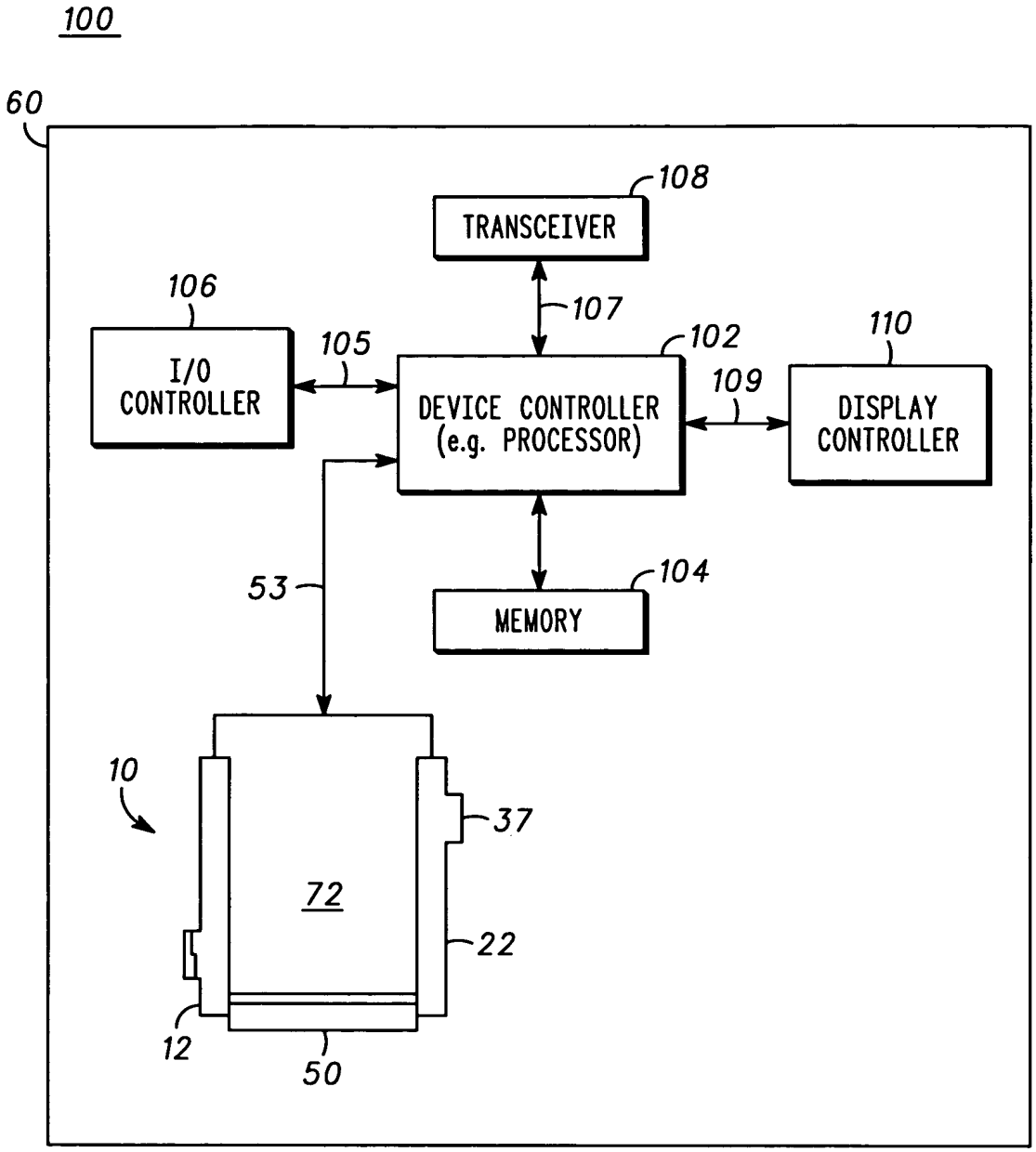
100
60
108
TRANSCEIVER
106
107
I/O CONTROLLER
105
102
110
DEVICE CONTROLLER (e.g. PROCESSOR)
109
DISPLAY CONTROLLER
53
104
MEMORY
10
37
72
22
12
50

FIG. 7